



# Market Announcement

17 December 2024

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## 4DS Memory Limited (ASX: 4DS) – Trading Halt

### Description

The securities of 4DS Memory Limited ('4DS') will be placed in trading halt at the request of 4DS, pending it releasing an announcement. Unless ASX decides otherwise, the securities will remain in trading halt until the earlier of the commencement of normal trading on Thursday, 19 December 2024 or when the announcement is released to the market.

### Issued by

ASX Compliance

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ASX RELEASE

17 December 2024

ASX Operations  
Level 40 Central Park  
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*Via Email*

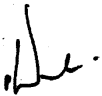
### TRADING HALT REQUEST

In accordance with ASX Listing Rule 17.1, 4DS Memory Limited (**Company**) requests that its securities be placed into an immediate trading halt.

In accordance with ASX Listing Rule 17.1, the Company advises that:

1. The trading halt is requested pending the release of an announcement regarding a development agreement, a Company update, and a capital raising.
2. The Company requests that the trading halt remains in place until the earlier of the release of an announcement or commencement of trading on Thursday, 19 December 2024.
3. The Company is not aware of any reason why this trading halt request should not be granted.
4. The Company does have any other information about this trading halt request that can be released prior to making the announcement to come out of the trading halt.

This request is authorised by the Board.



**Peter Webse**  
Company Secretary

**ENDS**

### Contact information

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**4DS Memory Limited (ASX: 4DS)**, with facilities located in Silicon Valley, is a semiconductor technology company bringing high bandwidth, high endurance, persistent non-volatile memory to advanced CMOS process nodes. Its technology, known as Interface Switching ReRAM, features tunable persistence and low energy per bit for today's most challenging compute intensive and AI processor applications. Established in 2007, 4DS owns a patented IP portfolio, comprising 34 USA patents, and is the first company to develop PCMO ReRAM, on an advanced CMOS processing node. 4DS has a development agreement with Belgium based imec – a world leading research and innovation hub in nano electronics and digital technologies, as well as a joint development agreement with Western Digital subsidiary HGST, a global leader in storage solutions.

For more information, please visit [www.4dsmemory.com](http://www.4dsmemory.com)