

ASX Announcement
18 August 2025

SIXTH PLATFORM LOT COMPANY UPDATE

4DS Memory Limited (ASX:4DS) (4DS) (the Company) today provides an update regarding the root cause analysis of the Sixth Platform Lot.

On 16 June 2025, the Company advised that initial analysis of wafers from the entire Sixth Platform Lot confirmed that the process modifications and optimisations implemented did not yield the anticipated outcomes in electrical testing and characterisation.

The Company also advised shareholders that it would undertake a root cause analysis to determine the factors behind the unexpected results observed in the initial analysis of the 20nm Sixth Platform Lot.

The Company has now completed a comprehensive root cause analysis of the 20nm Sixth Platform Lot, employing both physical and chemical techniques. The analysis confirmed that etch residues introduced during manufacturing led to electrical shorting of the memory devices.

As this is the Company's first manufacturing process to scale down to 20nm memory cells, which is two generations from the previous 60nm Platform Lot, further process optimizations will be necessary to succeed with this scaling from 60nm to 20nm memory cells. The ability to scale to 20nm as previously informed to shareholders is a significant goal in order to be able to demonstrate the potential of a generational memory technology.

The Company is now engaging with its partners to review these findings and jointly assess the most effective strategic and technical development pathways for 2025. The outcome of these discussions will guide the next stage of advancement, and shareholders will be updated as these initiatives progress.

4DS Interactive Investor Hub

If you have any questions on this announcement or any past 4DS announcements, check out our Investor Hub. Like, comment, ask a question and view video summaries on important announcements. Shareholders can link through to the Investor Hub via: announcements.4dsmemory.com

Authorised for release by the Board.

ENDS

Contact Information

Peter Webse, Company Secretary: pwebse@governancecorp.com.au

Investors:

David McAuliffe, Executive Chairman: info@4dsmemory.com

Sarah Lenard, Investor Relations, Narrat/IR: sarah@narratir.com.au

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About 4DS

4DS Memory Limited (ASX: 4DS), with facilities located in Silicon Valley, is a semiconductor technology company bringing high bandwidth, high endurance, persistent non-volatile memory to advanced CMOS process nodes. Its technology, known as Interface Switching ReRAM, features tuneable persistence and low energy per bit for today's most challenging compute intensive and AI processor applications. Established in 2007, 4DS owns a patented IP portfolio, comprising 34 USA patents, and is the first company to develop PCMO ReRAM, on an advanced CMOS processing node. 4DS has three important industry relationships: a development agreement with Belgium based imec, a world leading research and innovation hub in nano electronics and digital technologies; a design agreement with Infineon Technologies AG, Germany's largest semiconductor manufacturer and one of the ten largest semiconductor manufacturers worldwide; and a joint development agreement with Western Digital subsidiary HGST, a global leader in storage solutions.

For more information, please visit www.4dsmemory.com.

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