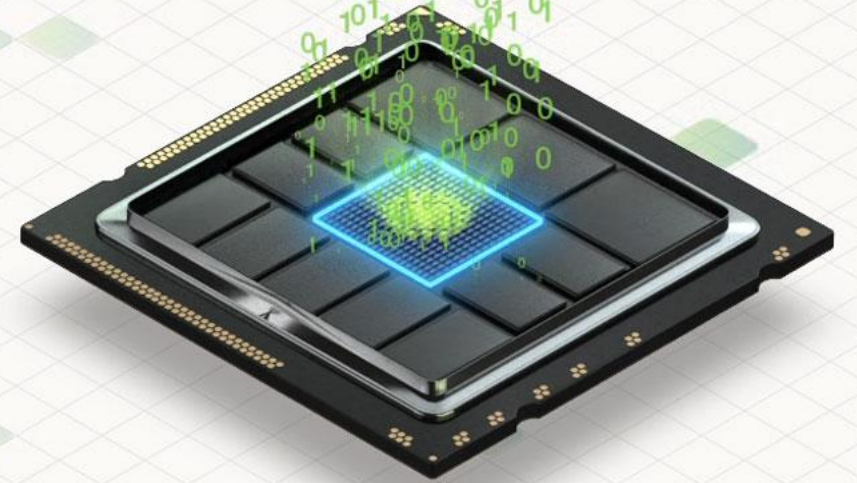




ersonal use only

Investor Update

October 2025



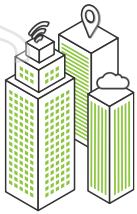


Introduction



Weebit Overview: Leading Vendor of ReRAM IP

Advanced Non-Volatile Memory (NVM) Now Entering Production



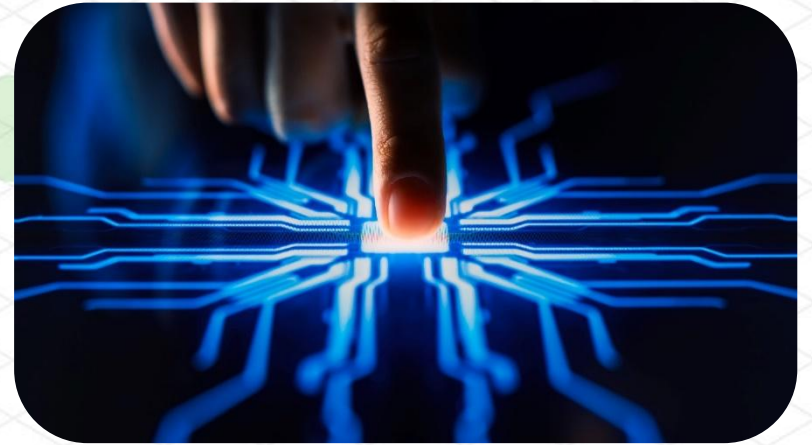
Founded: 2015

Located: Israel & France
>50 personnel (90% engineers/scientists; 13 PhDs)



Multiple commercial deals

Including tier-1 IDM onsemi; ongoing discussions/evals with >20 foundries/IDMs/product companies



Fast-growing markets

Edge AI, automotive, microcontrollers, power management/analog ICs...

Business model

IP licensing to semiconductor companies & fabs

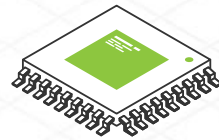


Proven, protected technology

Fully qualified per JEDEC and AEC-Q100; available for chip designers; >90 patents/applications

R&D partner

CEA-Leti, a leading microelectronics research institute



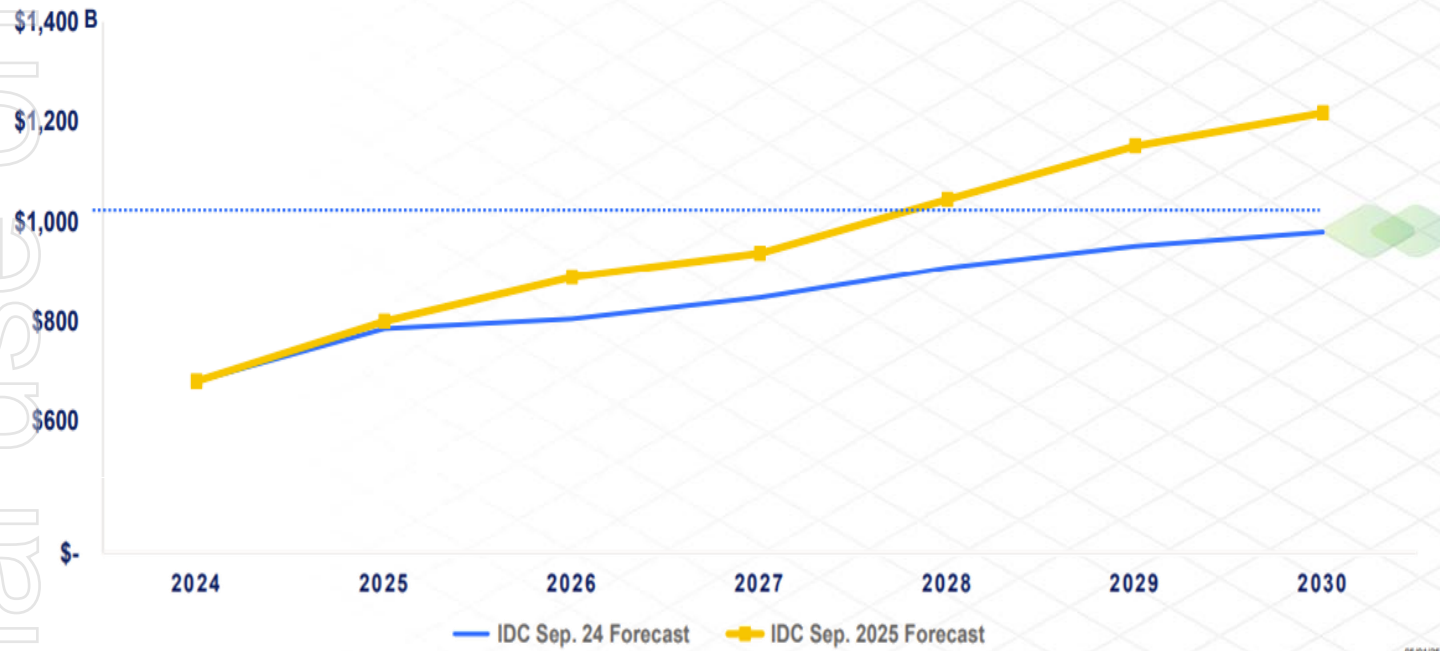
ASX: WBT

S&P/ASX 300

MSCI
Global Small Cap Index

Semiconductors: A Snapshot

AI Supercycle Accelerates \$1T Semiconductor Revenues Earlier Than Expected (~2 years)



Source: IDC CY2Q25 Semiconductor Application Forecast, Sep. 2025

05/01/25
Source: Company Filings, IDC Estimates



TSMC Intends to Expand Its Investment in the United States to US\$165 Billion to Power the Future of AI

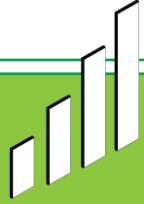


Nvidia to invest \$5bn in Intel after Trump administration's 10% stake



India is betting \$18 billion to build a chip powerhouse.

Embedded Non-Volatile Memory (NVM) is Strategic IP



Ubiquitous across Growing Applications

- AI, IoT, automotive, industrial, medical, consumer, defense
- Enables device intelligence, security, and resilience



Foundational to Modern Electronics

- Critical for:
- Firmware and code storage
 - AI weights and data tables
 - Configuration data and ID keys
 - Data logging



Drives Differentiation

- Performance: faster operation, better endurance
- Power consumption: Both run-time and sleep power
- System integration: Enabling single-chip solutions
- Secured: Prevent from chip hacking



Strategic across the Supply Chain

- Designers: simplify architecture; reduce BOM
- Foundries: added value vs. competition
- OEMs: smarter, more secure products
- Ecosystem: differentiate and monetize IP

A Brief History of Non-Volatile Memory (NVM)

~2010

Flash Scaling Issues
Known

Alternatives Emerge

~2020

Choices Narrow

2024

Superior
Tech Wins

Today

ReRAM is Chosen
Flash Replacement

FeRAM

MRAM

CBRAM

ReRAM

PCM

3Dxpoint

Optane

Too expensive
Difficult to implement
Exotic materials

MRAM

Mass
Production

ReRAM

Multiple
Companies
Developing

MRAM

Magnetic
Issues

ReRAM

In Production;
Vendors
Shake Out

TSMC redirects its
roadmap from MRAM
to ReRAM



The only
independent
ReRAM
supplier



Microelectronic Engineering
Volume 86, Issue 3, March 2009, Pages 283-286



Future challenges of flash memory
technologies



The Memory Guy Blog

Jim Handy, Objective Analysis, on Semiconductor Memories

Home About Jim Handy Privacy Policy

Month: June 2012

The End of Flash Scaling



ReRAM – Innovative NVM for a New Era



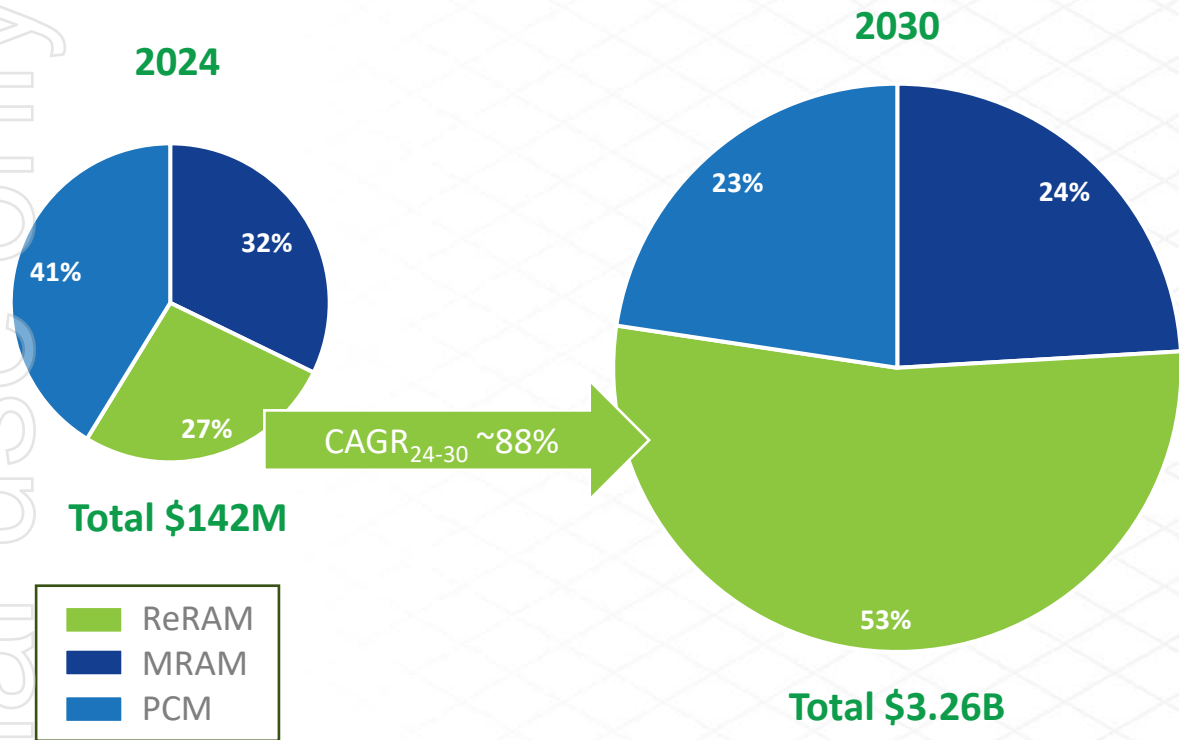
Weebit ReRAM

- ❖ Scales to advanced geometries where flash can't
- ❖ Ultra-low power
- ❖ Cost-effective
- ❖ Easy to integrate
- ❖ Highly reliable even at high temperatures
- ❖ Immune to EMI and radiation
- ❖ Fully qualified per JEDEC and AEC-Q100
- ❖ >90 patents/applications

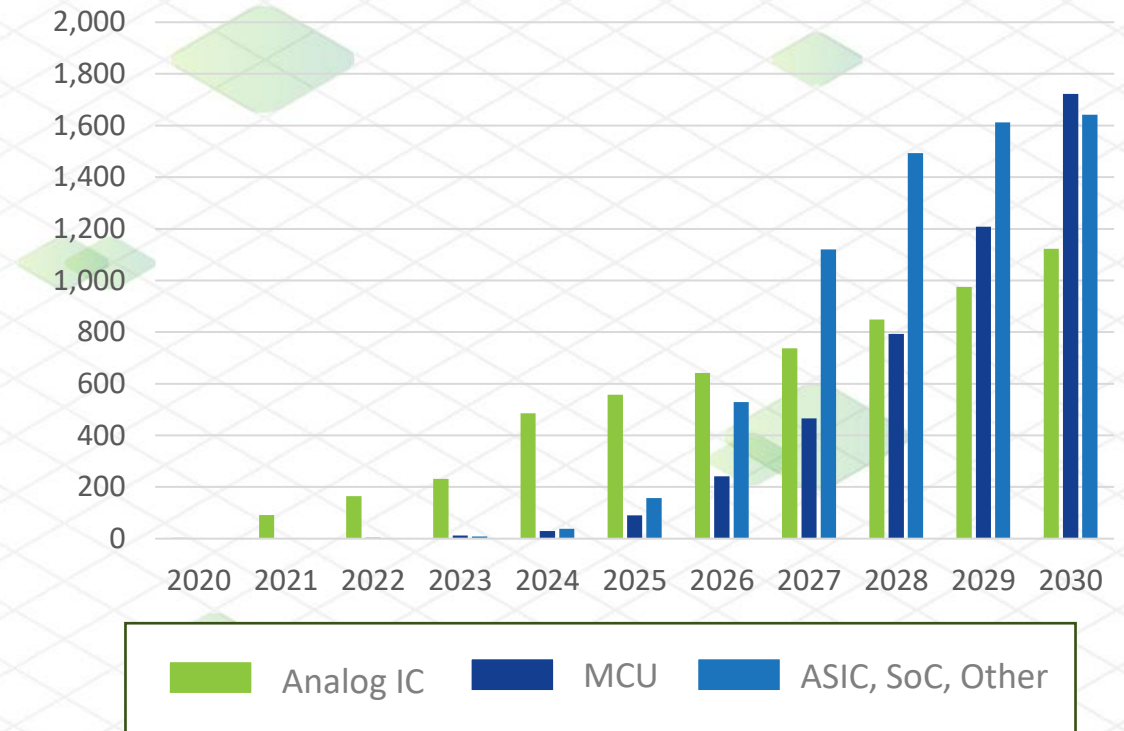
Weebit: The only independent provider of qualified ReRAM

The Rise of ReRAM

Embedded Emerging NVM Market (in \$M)



Embedded ReRAM Market Volume (# of Dies, in M units)



Note: The embedded emerging NVM market size is evaluated based on assumptions of the average chip area occupied by a given memory technology (Yole)

Source: Emerging Non-Volatile Memory report, Yole Group, 2025

Strong and Experienced Board and Management Team

Coby Hanoch
CEO



David (Dadi) Perlmutter
CHAIRMAN



Atiq Raza
NON-EXEC.
DIRECTOR



Dr. Yoav Nissan-Cohen
NON-EXEC. DIRECTOR



Naomi Simson
NON-EXEC.
DIRECTOR



Ashley Krongold
NON-EXEC.
DIRECTOR



Anne Templeman-Jones
DEPUTY CHAIR



Alla Felder
CFO



Ishai Naveh
CTO



Ilan Sever
VP R&D



Lilach Zinger
VP CUSTOMER SUCCESS



Issachar Ohana
CRO



Eran Briman
VP MARKETING &
BUSINESS DEV.





Recent Achievements



Recent Highlights



MAR 2025

Completed AEC-Q100 150°C operation qualification for automotive



AUG 2025

Achieved record revenue in FY25, an increase of 4X compared to the prior corresponding period

onsemi

SEP 2025

Taped-out first embedded module with Weebit ReRAM at onsemi's 300mm production fab



OCT 2025

Q1 FY26: record quarterly customer payments
Strong balance sheet with >A\$90 million cash at bank at end of quarter



OCT 2025

Achieved target of three product customers integrating Weebit ReRAM in next-generation products

onsemi Licenses Weebit ReRAM IP

Another proof point that ReRAM is the leading NVM choice for foundries/IDMs



onsemi provides intelligent power and sensing solutions with a focus on automotive and industrial

500

Fortune 500[®]
company
S&P 500[®] index

#2

Global position in
silicon power
solutions

100

Nasdaq 100[®]
company

\$7.1B

2024 revenue
(USD)

onsemi was spun out of Motorola as ON Semiconductor in 1999

onsemi data from the onsemi Quarterly Investor Presentation Fourth Quarter 2024

First Weebit ReRAM license to a top-tier Integrated Device Manufacturer (IDM)

- ❖ Currently being integrated into onsemi's Treo™ platform
 - ◆ Targets most advanced automotive and industrial applications
 - ◆ Combines Power Management, Compute and Sensing on a single die
 - ◆ Manufacturing 65nm BCD in a 300mm US production fab
- ❖ Weebit ReRAM will enable high-density NVM
 - ◆ Brings more intelligence to a range of onsemi's power products
 - ◆ Provides power-efficiency and cost-effectiveness
- ❖ September 2025 update: First tape-out of embedded ReRAM modules at onsemi 300mm US fab
 - ◆ Onsemi expected to use this in next-generation products

**IDMs manufacture their own products,
shortening time-to-production and potentially
accelerating time-to-royalties**

Weebit ReRAM in Automotive

Increasing number of semiconductors per car

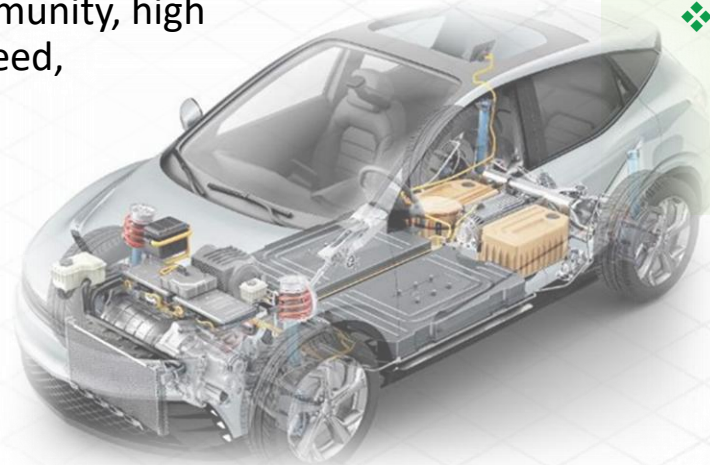
- ❖ ~1,000 - 3,000 chip per car; an electric Audi or Volkswagen contains up to 8,000 chips*

Automotive in need of new NVM technologies

- ❖ Growing needs for emerging NVM: code storage, trimming, data logging...
- ❖ Can effectively scale to advanced process nodes, <28nm

Parameters of Weebit ReRAM align with automaker specifications

- ❖ High-temp reliability, EMI immunity, high endurance, fast switching speed, longevity, secured



Completed AEC-Q100 150°C Operation Qualification

Key to getting designed into automotive MCUs & other components

- ❖ Demonstrated **stability at 150°C operation for up to 100K endurance cycles****
- ❖ Including cycling and post-cycling high-temp data retention

Beyond Automotive

- ❖ Many **industrial** and **IoT** applications require high-temp and extended endurance
- ❖ With AEC-Q100 we can service over 99% of applications

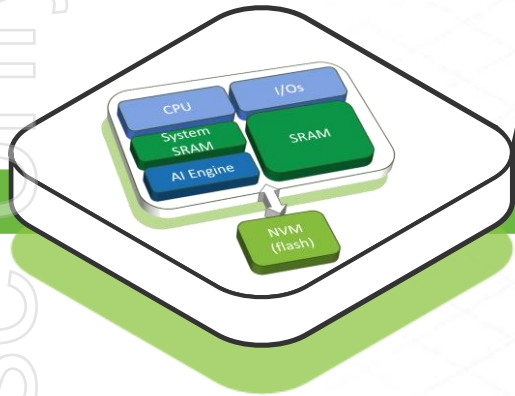
Completed qual in SkyWater S130 using module with 1T1R cell architecture

* <https://www.audi-mediacycenter.com/en/press-releases/semiconductors-are-becoming-the-neurons-of-our-cars-16053>

** Flash equivalent

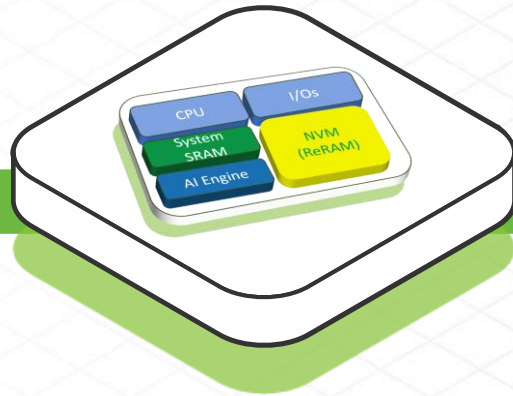
The Journey to New AI Architectures with ReRAM

TODAY'S TWO CHIP SOLUTION



- Wasteful in terms of size & cost
- Prohibitive power
- Constrained data bandwidth and performance
- Insecure, vulnerable to hacking

SINGLE CHIP NEAR-MEMORY COMPUTE



- External NVM eliminated
- Eliminates most data movements → Low-power
- More efficient storage: **4X greater capacity** than SRAM

IN-MEMORY COMPUTE WITH RERAM



- Computation performed *within* memory arrays
- **Completely eliminates** data traffic → Ultra-low power
- Enables **Analog Compute** → Store larger models efficiently

NEUROMORPHIC COMPUTE WITH RERAM



- Future systems will mimic brain behavior
- Fast real-time processing on **massive amounts of data**
- Three orders of magnitude **(x1000) better energy efficiency**

ReRAM-based architectures are central to the transformation of AI, **bringing memory and compute together** for faster, more brain-like intelligence

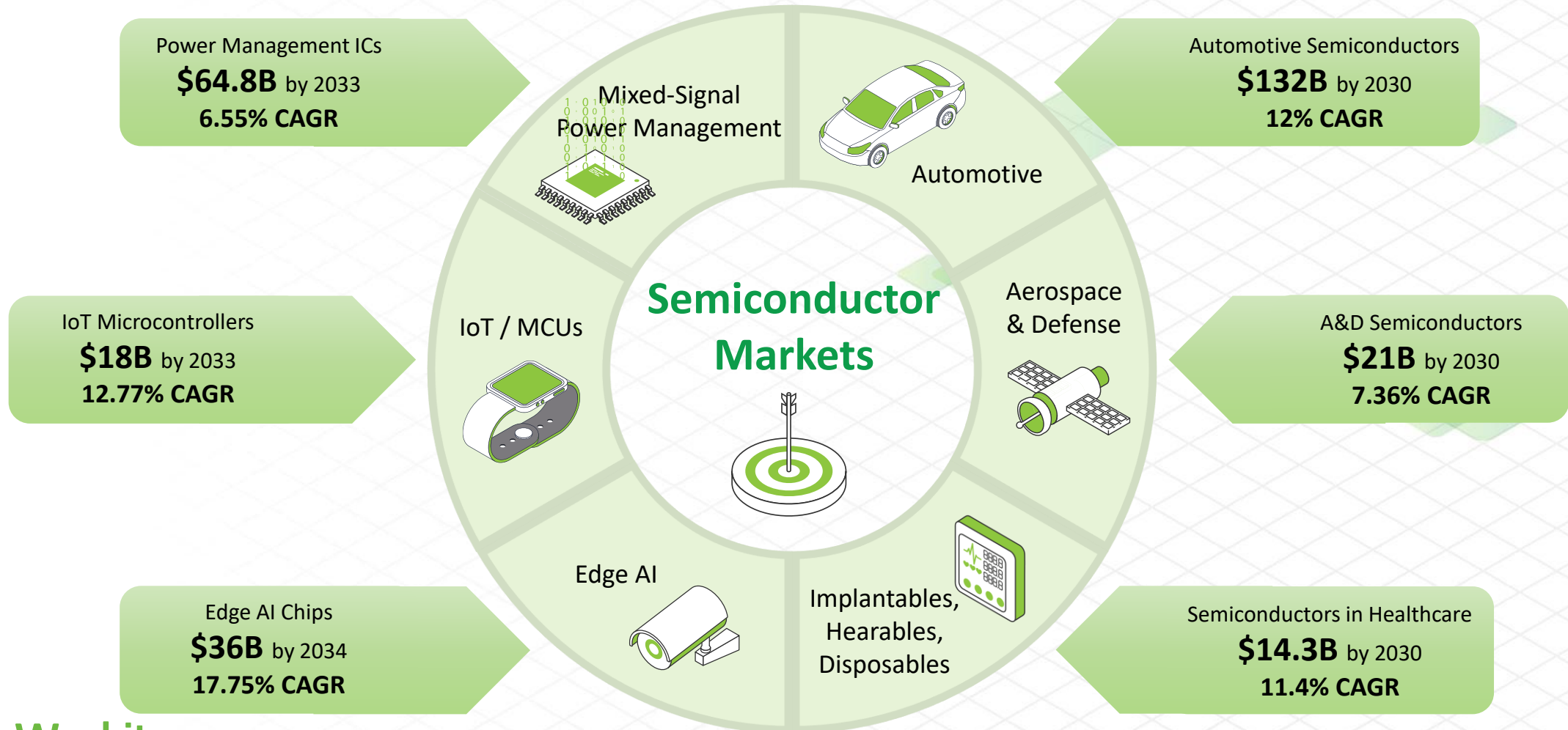


Opportunities Ahead

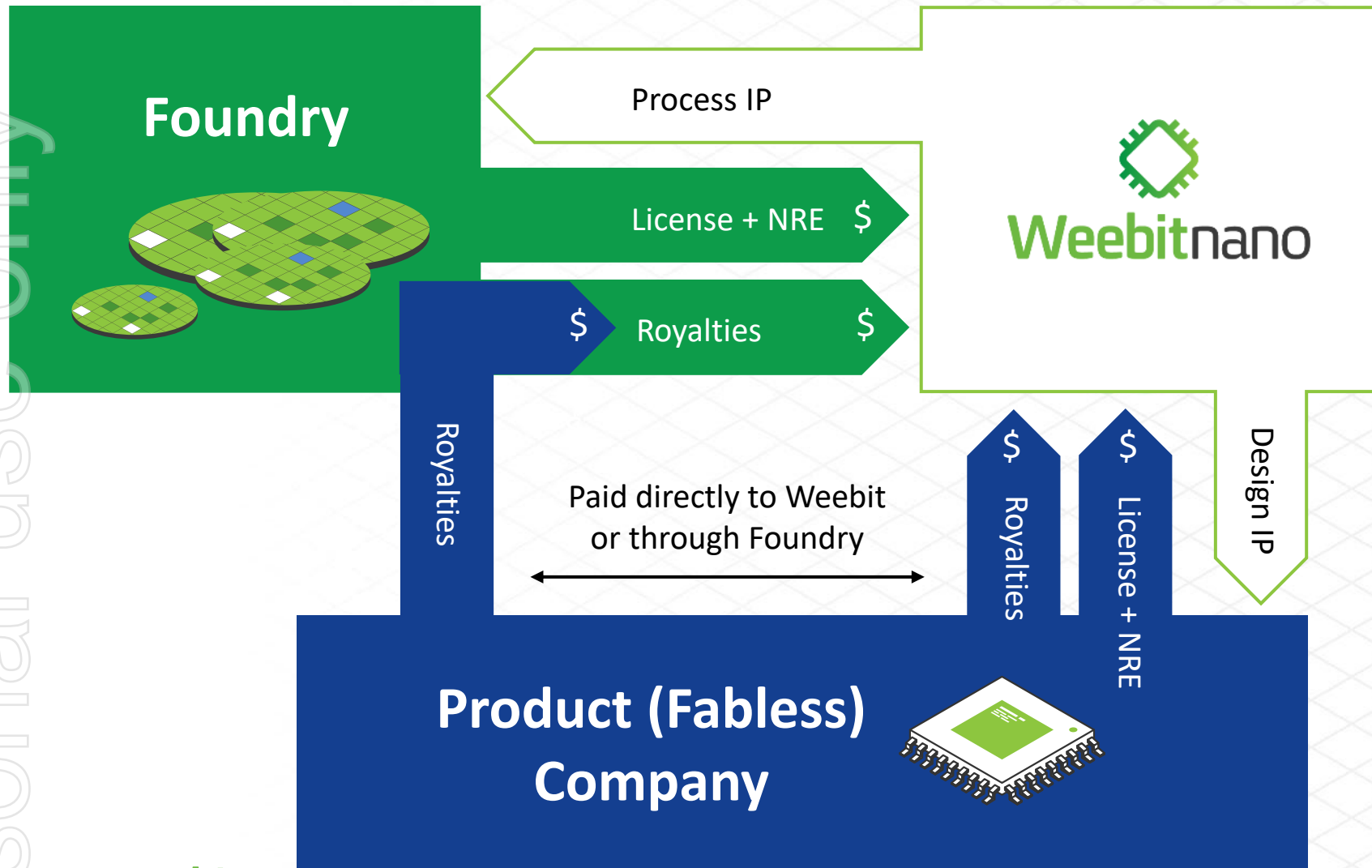


Addressing Fast-Growing Semiconductor Markets

>US\$265 Billion TAM in Customer Markets



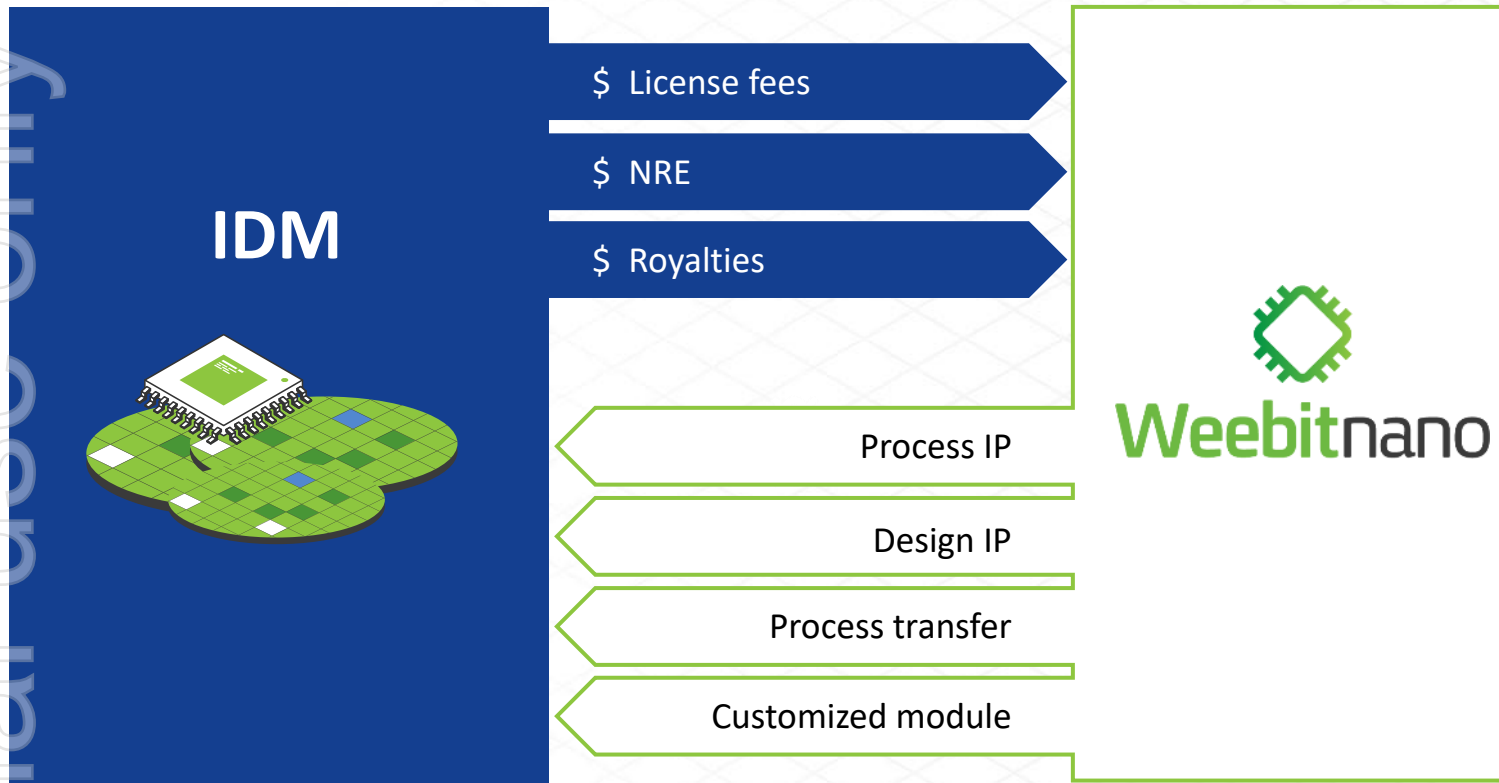
IP Business Model – Foundries & Fabless



Benefits of IP Model

- ❖ Low COGS
 - ◆ Direct costs are associated with supporting and maintaining IP
 - ◆ No inventory
- ❖ High gross and operating margins
 - ◆ Once IP is created, it gets sold multiple times
 - ◆ Royalties go directly to the bottom line

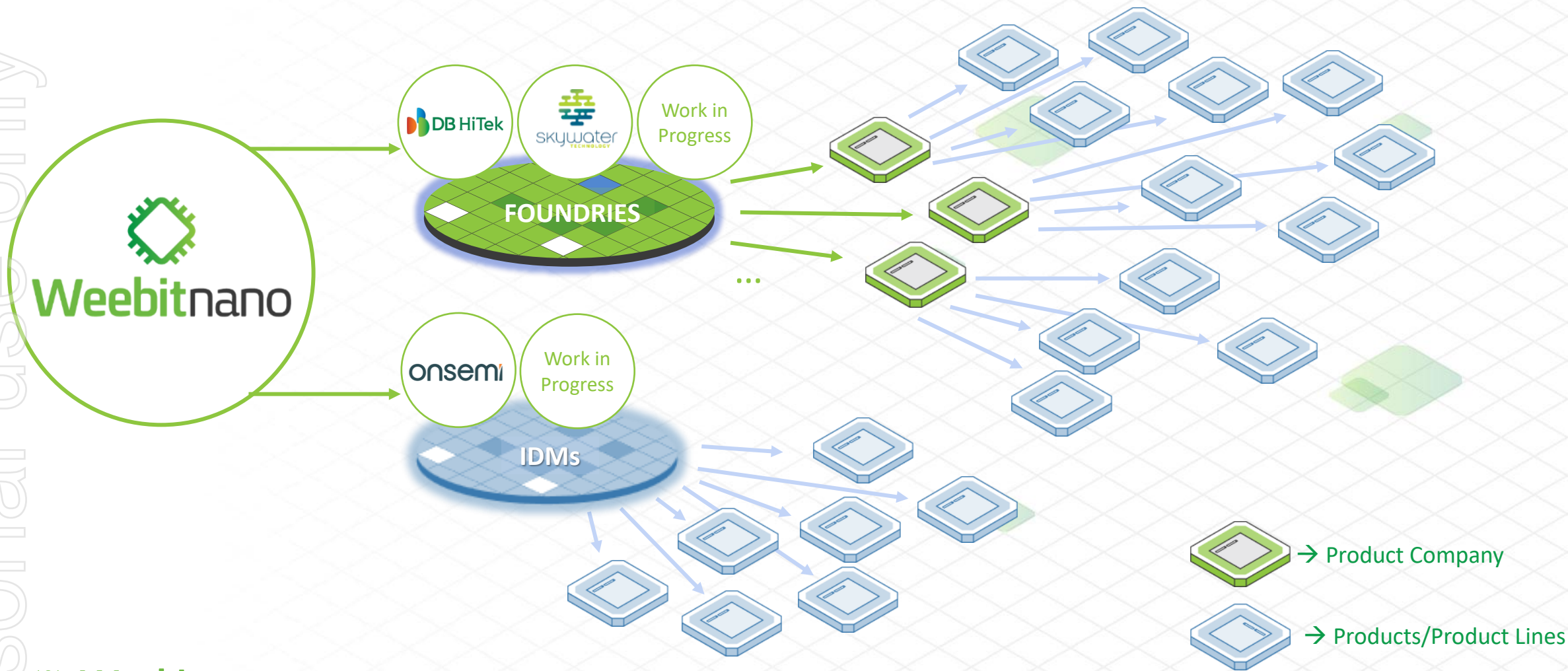
IP Business Model – Benefits with IDMs



- ❖ IDMs design, manufacture and sell their own devices
- ◆ Develop products quickly for target end markets
- ◆ Can integrate IP in multiple designs / products

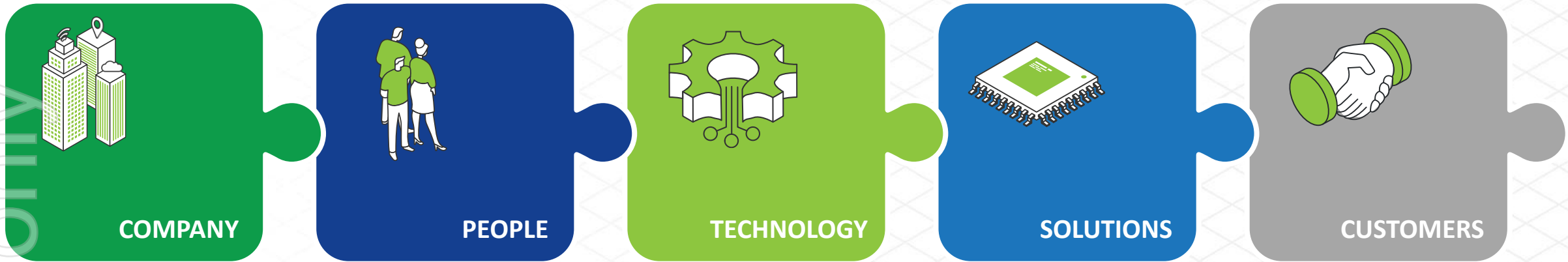
Accelerated
time-to-market and
time-to-revenue

Every Foundry Deal Represents Multiple Customer Opportunities





ersonal use only

Weebit's Differentiation



- Only **independent** provider of **qualified** ReRAM technology
- **ASX 300** (ASX:WBT)
- >A\$90 cash e/o 1Q FY26

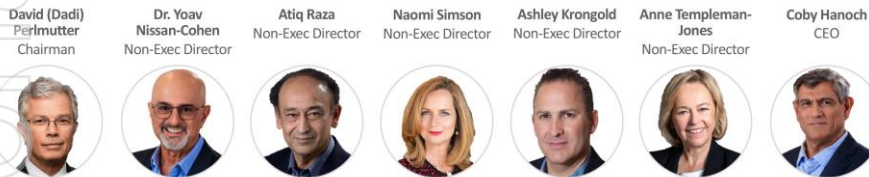
- Located: Israel & France
- >50 personnel (**90% engineers**; 13 PhDs)
- R&D partner  
- Experienced management

- Low-power, cost-effective, reliable NVM
- Scales where flash can't
- Targeting **fast-growing markets**: edge AI, automotive, power / analog ICs, MCUs, ...
- Qualified and ready for production

- Licensing **Semiconductor IP** for embedded markets
- Research towards **IMC and neuromorphic**
- Exploring solutions for future **discrete markets**
- Unrivalled design team delivers **high-quality IP**; enables **fast time-to-market** for customers

- **Multiple commercial deals**, including tier-1 IDM onsemi and top-10 foundry DB HiTek
- 
- Ongoing discussions and evals with **>dozen vendors** (foundries, IDMs, product companies)
- **Several products in design** with Weebit ReRAM

WORLD-RENOWNED BOARD OF DIRECTORS



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THE NEXT NVM IS HERE

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