1 June 2026

dorsaVi's RRAM Showcases Stable Performance In Temperature Testing at 150°C

Evaluation demonstrates predictable and reversible RRAM cell behaviour across thermal conditions aligned with AEC-Q100 automotive-grade requirements

Key Highlights:

- **Stable Performance at 150°C Temperature Testing:** Successfully demonstrated predictable and fully reversible RRAM cell behaviour at operating temperatures up to 150°C, proving the technology's absolute reliability under extreme real-world thermal conditions.

- **Rigorous Testing Mechanisms with Industry Standard Methodologies:** Evaluated across a symmetrical heating-cooling sequence matching the stringent AEC-Q100 automotive-grade reliability standards, establishing a clear pathway for commercial vehicle and industrial deployment.

- **Stable Performance in Symmetrical Heating-cooling Sequence from Room Temperature to 150°C and 150°C Room Temperature:** Symmetrical heating-cooling testing confirmed that heat-induced shifts in resistance are entirely temporary and recover fully upon cooling, proving zero permanent physical degradation or thermal wear to the RRAM cells.

- **Potential for a temperature aware RRAM design:** Establishes a foundation for dynamic, temperature-aware voltage optimization, allowing future chips to scale down power consumption and eliminate unnecessary electrical overdrive.

- **Unlocks High Value Applications such as Exoskeletons and Robotics:** Enables reliable, continuous memory operation in sealed, uncooled environments where compute components sit directly adjacent to high-heat joint motors, actuators, and high-discharge batteries.

Melbourne, Australia, 1 June 2026 – dorsaVi Limited (ASX: DVL) ("dorsaVi" or the "Company") is pleased to provide an update on the development and testing of its RRAM technology. The Company has completed a high-temperature evaluation of RRAM cell operation, demonstrating predictable and reversible behaviour at operating temperatures up to 150°C.

The result is a meaningful step for DVL's RRAM technology. It confirms the technology can operate reliably under temperatures found in real-world, heat-exposed hardware such as robotics, exoskeletons, autonomous systems and industrial equipment, with the test conditions aligned to the AEC-Q100 automotive-grade reliability standard. The temperature testing showed that the heat-induced effects did not degrade the RRAM cells, opening up applications to high-value industries and applications.

Stable Performance at 150°C Temperature Testing

The evaluation confirms that, while RRAM is not a charge-based memory, its read and write behaviour remains temperature dependent due to underlying electronic and ionic transport mechanisms. The observed response is perfectly consistent with RRAM cell physics, demonstrating metallic-like filamentary conduction in the low-resistance state (LRS) and thermally activated transport temperature in the high-resistance state (HRS).

The high-temperature evaluation demonstrated:

- **Predictable Read Margins:** The effect of heat on the RRAM affected how clearly it distinguished its stored states (the resistance window) in a predictable and temporary manner. The measurements show that lower-resistance and higher-resistance state regions respond differently to temperature, which can influence the resistance window and read margin.
- **Zero Thermal Degradation:** Once the cells cooled back to room temperature, performance returned entirely to its starting baseline, confirming the heat caused no permanent physical damage or structural wear.
- **Flawless Physical Alignment:** Device response perfectly matched established memory physics with no unexpected anomalies, proving that temperature affects dorsaVi's RRAM in a way that can be predicted, measured, and calibrated.

Critically, symmetrical heating (room temperature to 150°C) and cooling (150°C to room temperature) tests showed that the resistance range recovered after cooling, indicating that the temperature-induced shifts were reversible rather than permanent degradation. The measurements show that lower-resistance and higher-resistance state regions respond differently to temperature, which can influence the resistance window and read margin.

Rigorous Testing Mechanisms with Industry Standard Methodologies

The evaluation employed the AEC-Q100 automotive-grade industry standard testing methodology across a symmetrical heating-cooling sequence:

- **Symmetrical Thermal Profile:** Testing was conducted incrementally at RT, 85°C, 105°C, 125°C, and 150°C, then cooled symmetrically back to RT through the same checkpoints to assess reversibility.

- **Multi-Point Read Current Assessment:** Rather than a single read-point measurement, representative read-current checkpoints were assessed across the full resistance window at each thermal step, providing a more complete picture of temperature-dependent read-margin behaviour and a stronger basis for calibrating future commercial chip designs.

Potential for a temperature-aware RRAM Architecture

This predictable temperature response is important for practical deployment, as it enables sensing margins, calibration strategies, and system design to account for real-world operating conditions. The measured temperature dependence also creates a pathway for temperature-aware voltage optimisation, where read thresholds, write voltages, or pulse widths may be adjusted according to operating temperature, potentially reducing unnecessary electrical overdrive, lowering energy consumption while preserving reliable switching and read-margin performance.

Mathew Regan, Group Chief Executive Officer of dorsaVi, said:

"Demonstrating reliable RRAM operation at 150°C is a significant step in our commercialisation program. It confirms the technology is built for the environments our target applications actually operate in, and it opens up design approaches that were not available with conventional memory. We are pleased with the progress and remain focused on advancing the platform toward tape-out."

This release has been authorised for lodgement to the ASX by the Board.

- **ENDS** -

Mathew Regan **Group Chief Executive Officer** **+61 427 477 298** **Email:** mregan@dorsaVi.com	**Gernot Abl** **Chairman** **+61 419 802 653** **Email:** ga@dorsaVi.com

Forward-Looking Statements

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About dorsaVi

dorsaVi Ltd (ASX: DVL) is an ASX company focused on delivering intelligence at the ultra-edge. Enabling real time AI-driven decisions to be made locally, at the point of sensing, without reliance on cloud connectivity. dorsaVi's wearable sensor technology captures, quantifies, and assesses detailed human movement and position outside a biomechanics lab, in both real-time and real situations for up to 24 hours, across clinical applications, elite sports, and occupational health and safety. Underpinning this vision, dorsaVi is building the hardware foundations of the ultra-edge through strategic investments in neuromorphic computing and RRAM memory technology. dorsaVi's focus is on three major markets:

- Ultra-Edge Intelligence: dorsaVi's sensor platforms are designed to process and act on data locally, embedding AI-driven inference directly at the point of capture. By investing in neuromorphic computing and RRAM memory technology, dorsaVi enables real-time decision-making without round-tripping to the cloud, delivering lower latency, lower power consumption, and reliable operation in latency- and connectivity-constrained environments across industrial, clinical, and autonomous systems applications.

- Workplace: dorsaVi enables employers to assess risk of injury for employees as well as test the effectiveness of proposed changes to OHS workplace design, equipment or methods based on objective evidence. dorsaVi works either directly with major corporations, or through an insurance company's customer base with the aim of reducing workplace compensation and claims. dorsaVi has been used by major corporations including London Underground, Vinci Construction, Crown Resorts, Caterpillar (US), Boeing, Monash Health, Coles, Woolworths, Toll, Toyota, Orora, Mineral Resources and BHP.

- Clinical: dorsaVi is transforming the management of patients with its clinical solutions (ViMove+) which provide objective assessment, monitoring outside the clinic and immediate biofeedback. The clinical market is broken down into physical therapy (physiotherapists), hospital in the home and elite sports. Hospital in the home refers to the remote management of patients by clinicians outside of physical therapy (i.e. for orthopaedic conditions). Elite sports refer to the management and optimisation of athletes through objective evidence for decisions on return to play, measurement of biomechanics and immediate biofeedback to enable peak performance.

Further information is available at www.dorsavi.com